Academic Year 2017-2018

Workshop / Seminar / FDP:

S.No	Nature of the Programme	Date	Title of the Programme	Speakers
1.	Seminar	12.03.2018	Intellectual Property Protections for Embedded Software.	Mr K. B. Pavan kumar, IT Business Analysts, Mindtree Ltd,Chennai.
2.	Seminar	24.01.2018	Research Methodology on Digital Signal Processing in MATLAB.	Mr S. Gokulakrishnan, Senior Software Engineer, HCL Technologies,Chennai.
3.	Seminar	15.09.2017	Advanced Research Methodology in VLSI Design.	Mr. M. Thiyagarajan, Senior Embedded Engineer, ELLCRAFTS,Chennai.

Value Added Courses:

S.No	Nature of the Programme	Title of the Course	Duration Of Course	Name of Course Coordinator
1.	Value Added course.	Digital Switching in Modern Constructions.	33	G.kadirvel
2.	Value Added course.	Hands on Experience in Wireless Broad Band Communication	32	D.Arun
3.	Value Added course.	Combined Applications of High level processors with sensors.	35	P.Arivuselvi.